

Block diagram

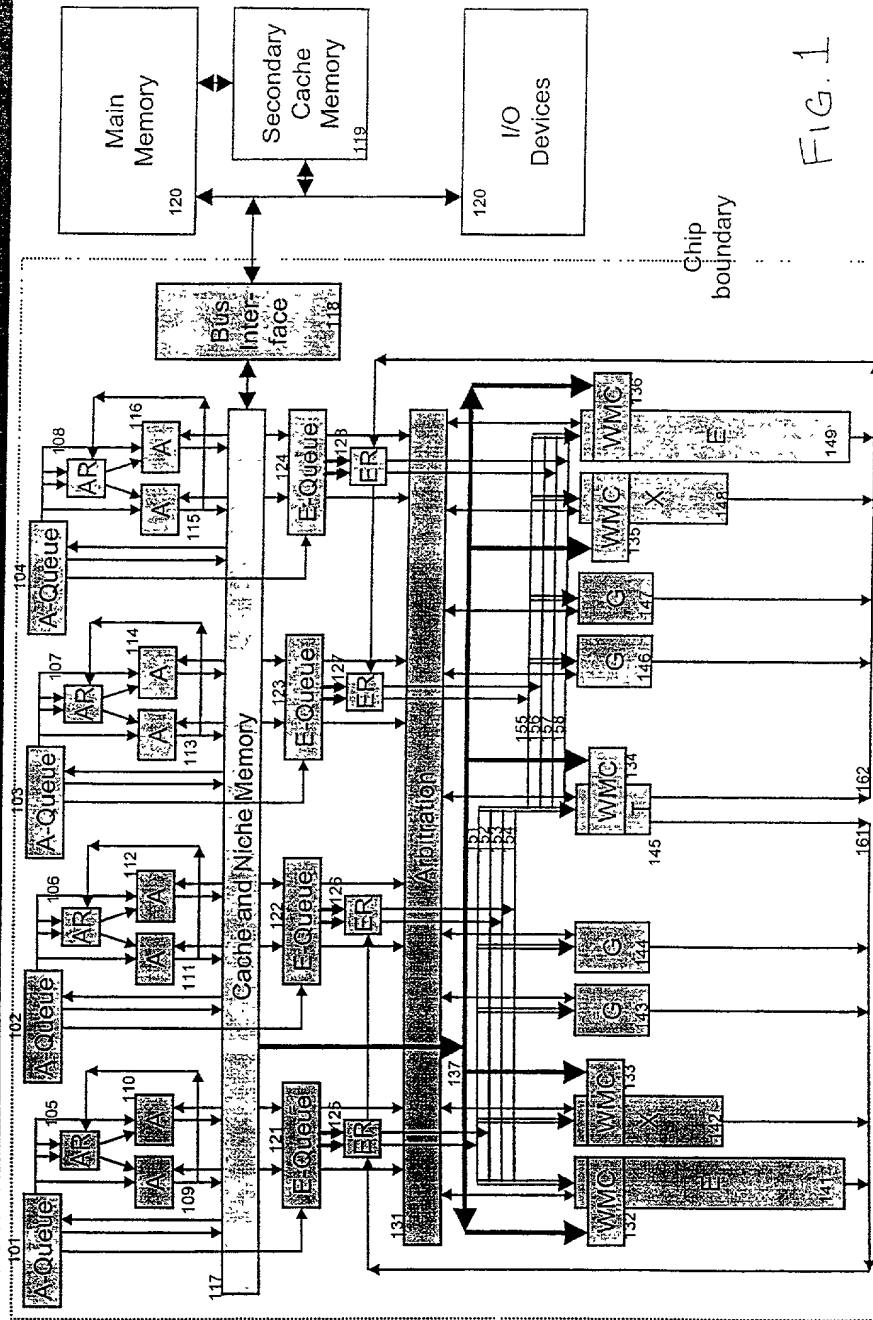


FIG. 1

Wide multiply matrix

$$\blacksquare \text{rd}_{128} = \text{m}[\text{rc}]_{(128 \times 64/\text{size})} * \text{rb}_{128}$$

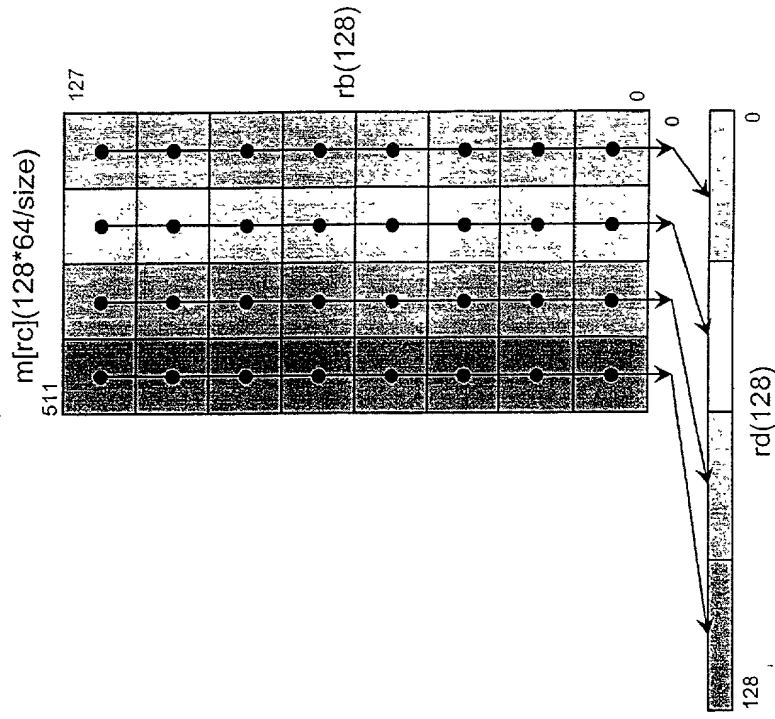
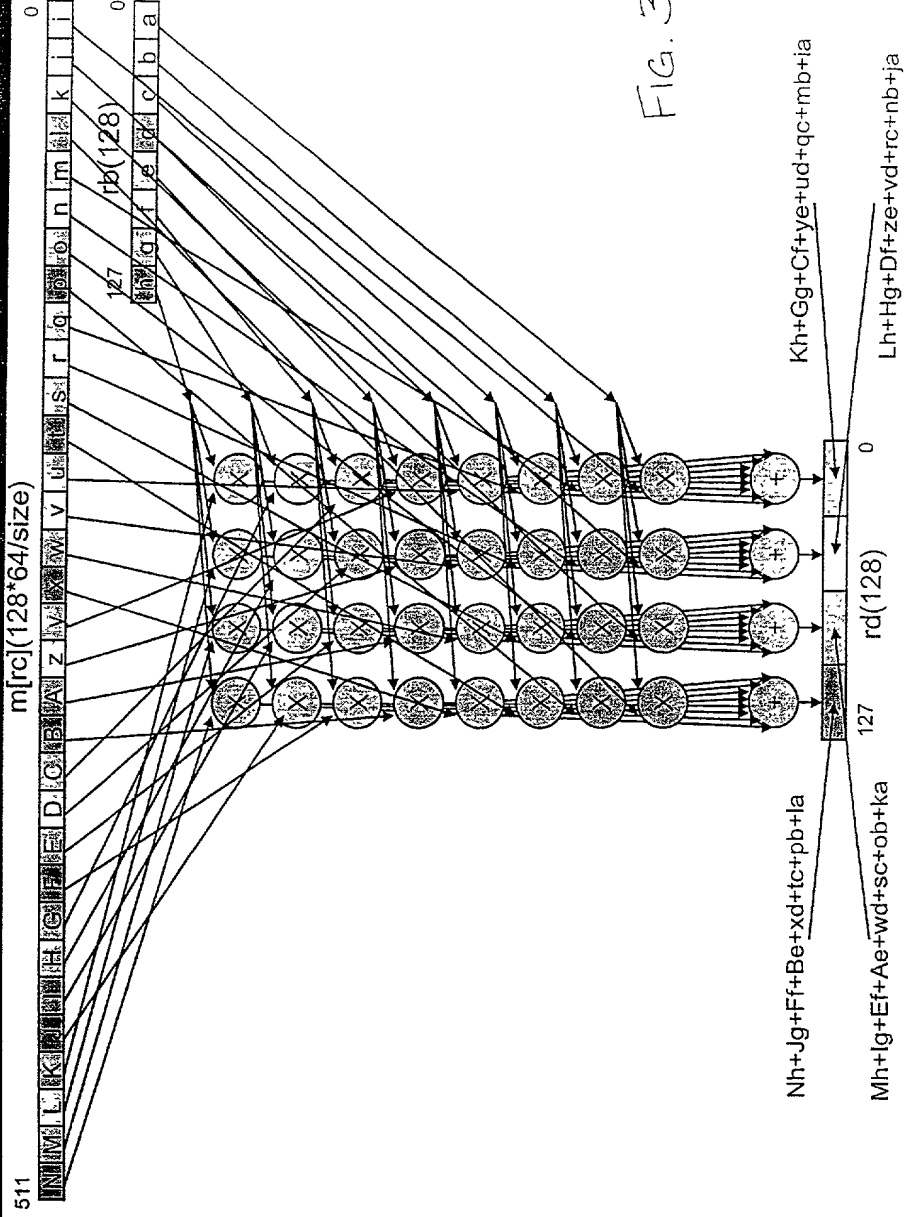
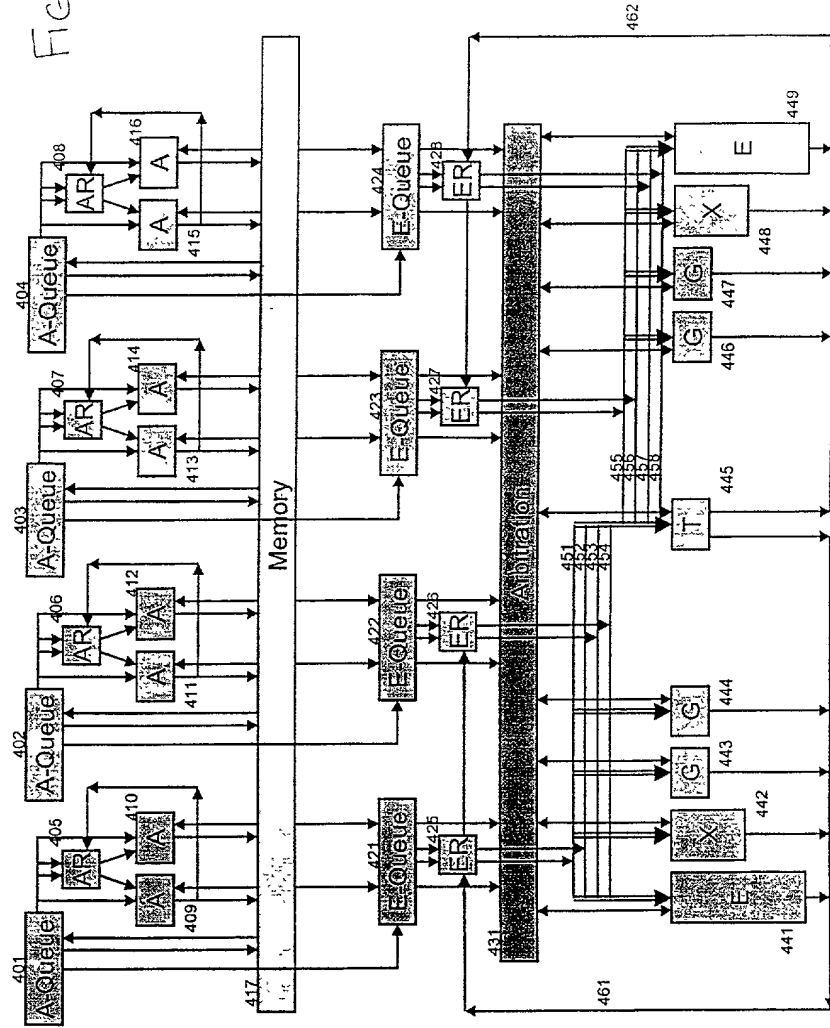


FIG 2

Wide multiply matrix



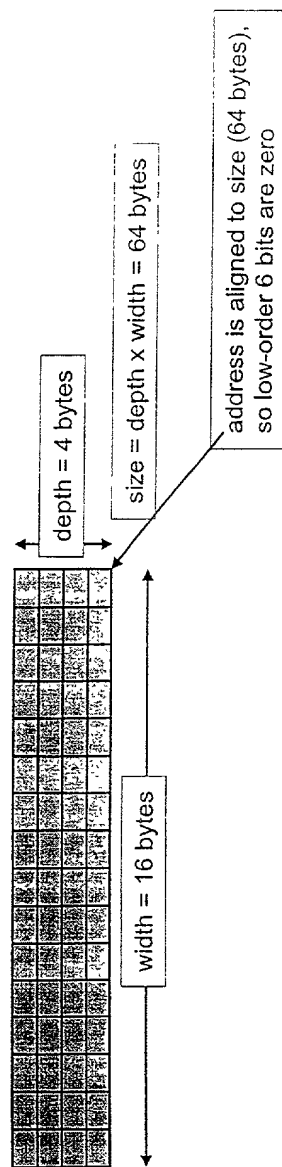
SMT + DAE



Wide operand specifier

Fig. 5

```
■ specifier=address+(size/2)+(width/2)
```



address

size/2

width/2

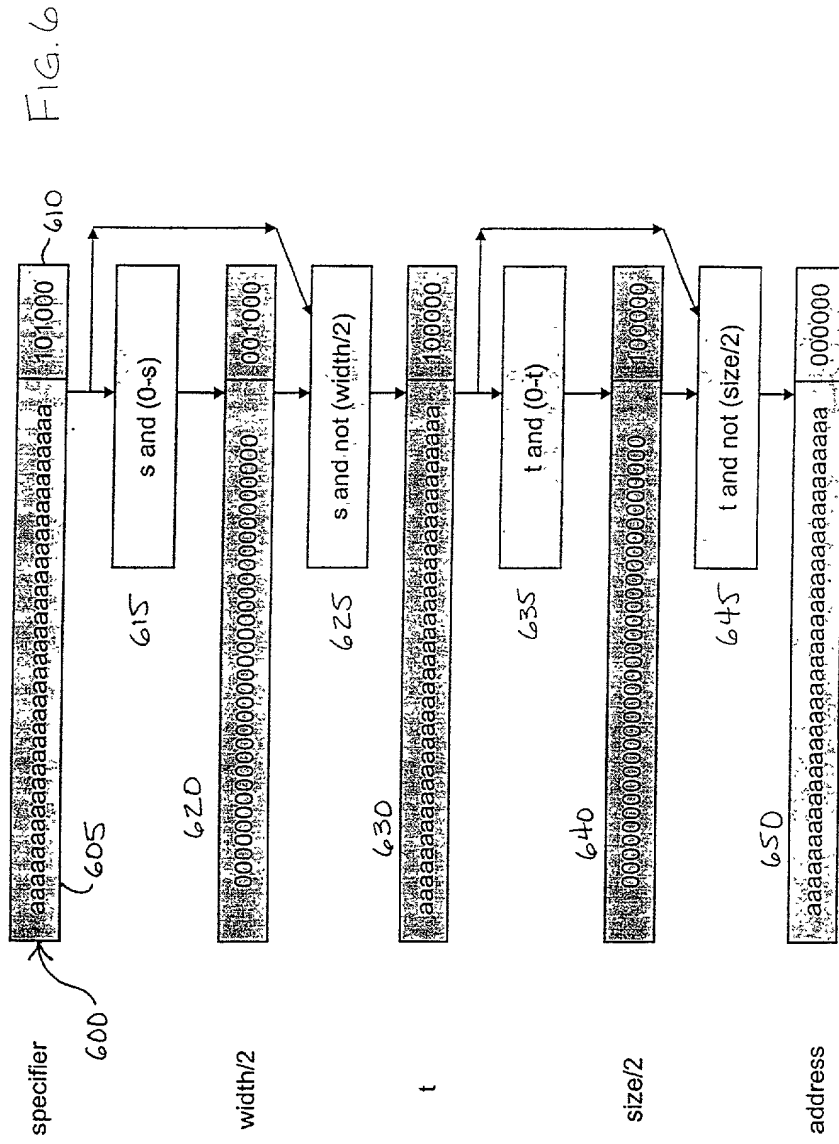
specifier

500 →

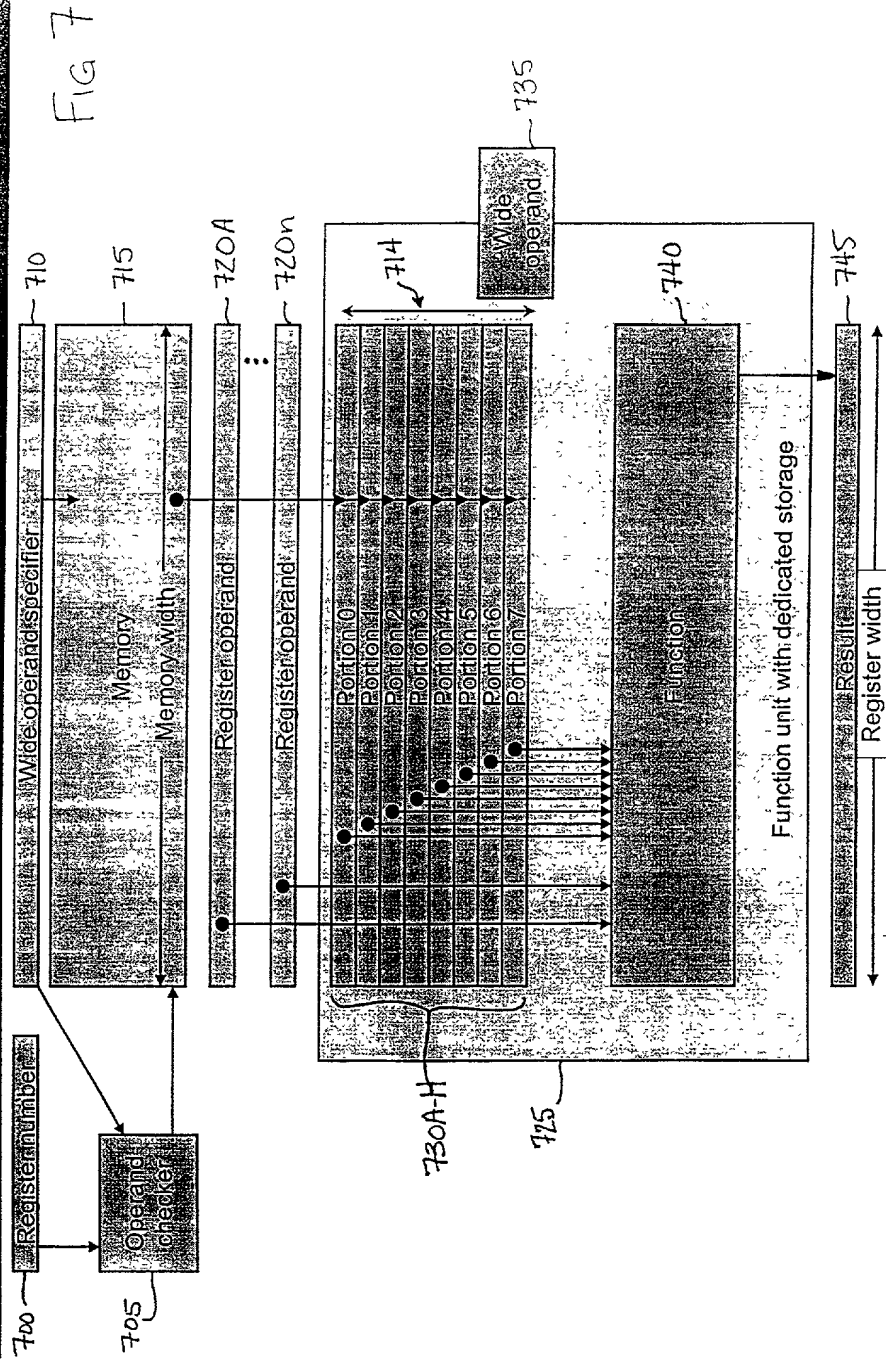
505

-510

Specifier decoding

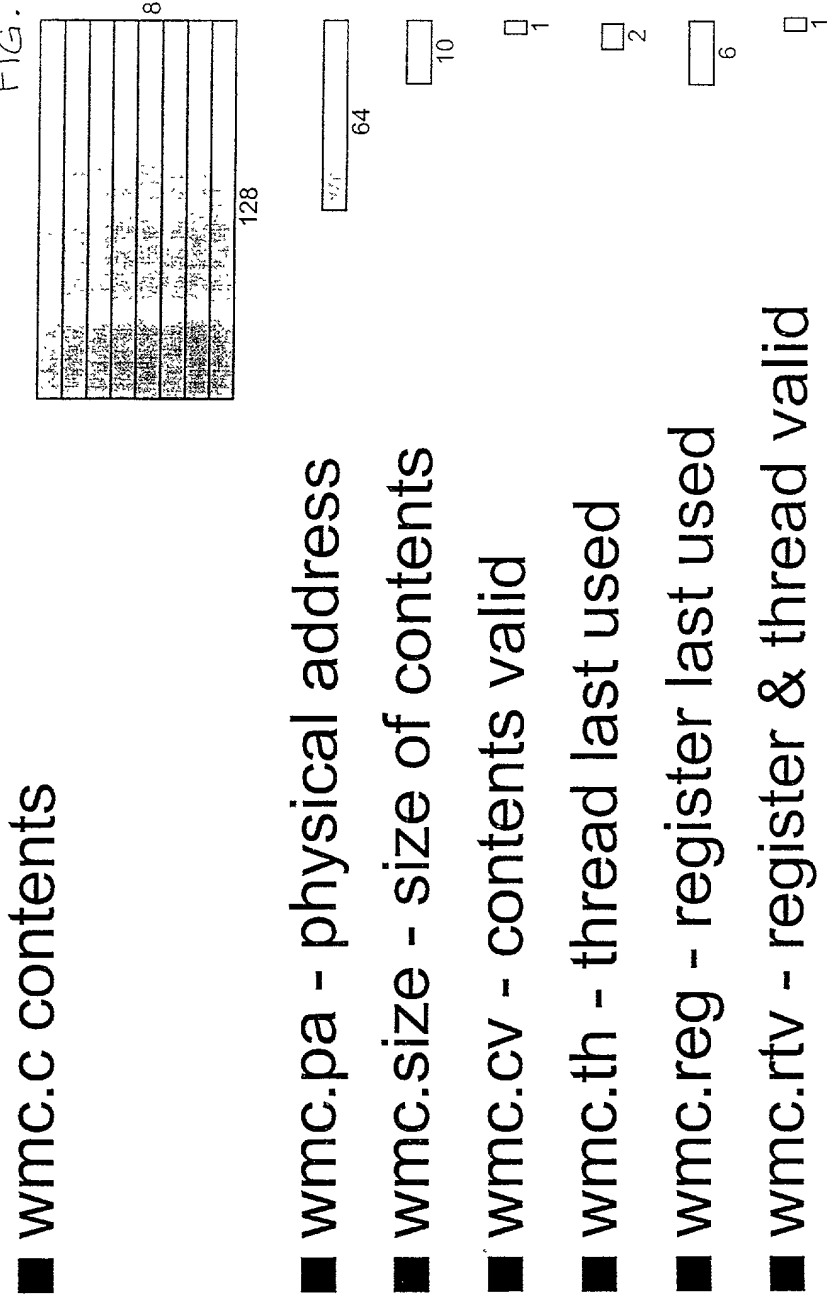


Wide function unit



Wide MicroCache data structures

FIG. 9



Wide MicroCache control (2)

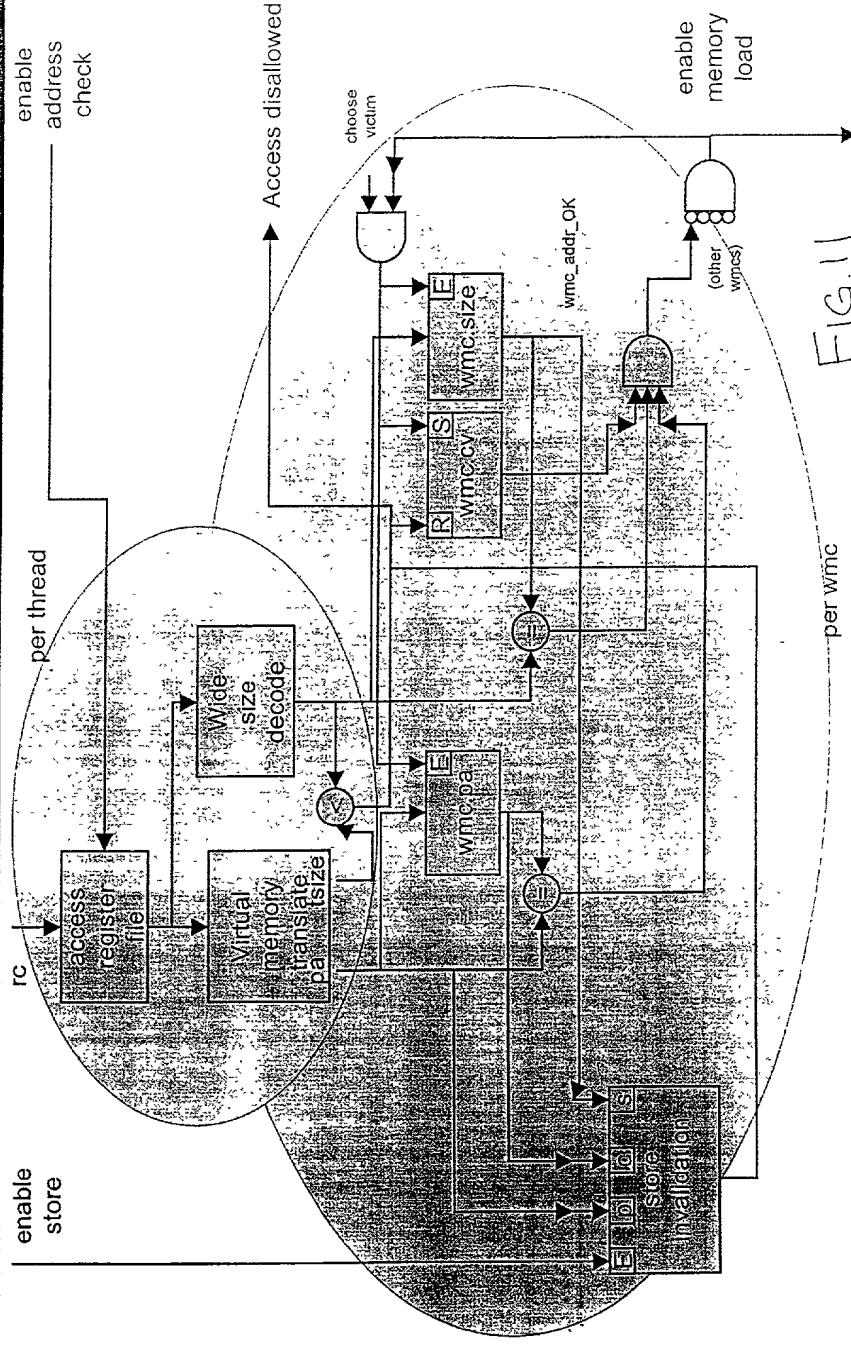


FIG.11